

THW  
Attorney Docket No. 030439

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re Application of )

Patel et al. )

Serial No. 10/750,342 )

Filed: December 31, 2003 )

For: IDENTIFYING PROCESS AND  
TEMPERATURE OF SILICON  
CHIPS

) Group No. 2857

**REQUEST FOR CORRECTION OF OFFICIAL FILING RECEIPT**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Attn: OFFICE OF INITIAL PATENT EXAMINATION'S CUSTOMER SERVICE CENTER

Dear Sir:

Upon review of the Official Filing Receipt and Updated Filing Receipt, an error appears in the Domestic Priority data as claimed by applicant. Applicants request that this error be corrected and that a new Official Filing Receipt be issued.

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on:

August 27, 2004

(Date of Deposit)

Theresa Badet

(Name of the Person Making Deposit)

*Theresa Badet*  
(Signature)

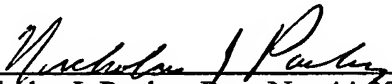
Enclosed herewith is a copy of the Combined Declaration/Power of Attorney filed on June 30, 2004. As listed on the Combined Declaration/Power of Attorney, the application claims priority to 60/525,103 filed November 24, 2003 AND 10/722,350 filed November 25, 2003.

Also enclosed, is a copy of the first page of patent application indicating the priority claim as well as a copy of the incorrect Official Filing Receipt showing the correction to me made in red ink.

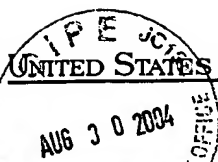
Applicants submit that the above-indicated error is an error made on the part of the Office, rather than by the Applicants, and respectfully request that the noted error in the priority claim of the application be corrected and a new Official Filing Receipt be issued to Applicants.

Respectfully submitted,

Dated: August 27, 2004

By:   
Nicholas J. Pauley, Reg. No. 44,999  
Phone No. (858) 845-8405

QUALCOMM Incorporated  
Attn: Patent Department  
5775 Morehouse Drive  
San Diego, California 92121-1714  
Telephone: (858) 658-5787  
Facsimile: (858) 658-2502



## UNITED STATES PATENT AND TRADEMARK OFFICE

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APPL NO.	FILING OR 371 DATE	PART UNIT	FIL FEE REC'D	ATTY. DOCKET NO	DRAWINGS	TOT CLMS	IND CLMS
10/750,342	12/31/2003	2857	1202	030439	7	32	4

CONFIRMATION NO. 9469

## UPDATED FILING RECEIPT



\*OC000000013412885\*

23696  
 Qualcomm Incorporated  
 Patents Department  
 5775 Morehouse Drive  
 San Diego, CA 92121-1714

COPY

Date Mailed: 08/02/2004

Receipt is acknowledged of this regular Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Filing Receipt Corrections, facsimile number 703-746-9195. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

## Applicant(s)

Jagrut Viliskumar Patel, San Diego, CA;  
 Martin Vyungchon Choe, San Diego, CA;  
 Ziad Mansour, Poway, CA;

## Domestic Priority data as claimed by applicant

This appln claims benefit of 60/525,103 11/24/2003

and 10/722,350 11/25/03

## Foreign Applications

If Required, Foreign Filing License Granted: 05/03/2004

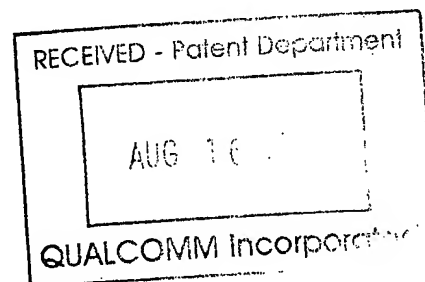
Projected Publication Date: 05/26/2005

Non-Publication Request: No

Early Publication Request: No

## Title

Identifying process and temperature of silicon chips



**Preliminary Class**

702

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Title 35, United States Code, Section 184  
Title 37, Code of Federal Regulations, 5.11 & 5.15**

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